

- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

### Product Summary



BVDSS	RDS(on)	ID
20V	80mΩ	1.6A

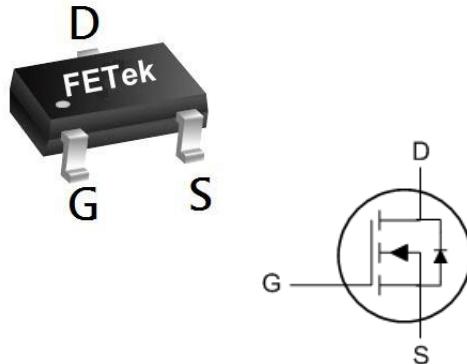
### Description

The FKUA2544 is the high cell density trenched N-ch MOSFETs, which provides excellent RDS(on) and efficiency for most of the small power switching and load switch applications.

The FKUA2544 meets the RoHS and Green Product requirement with full function reliability approved.

### Absolute Maximum Ratings

### SOT323 Pin Configuration



Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	20	V
V <sub>GS</sub>	Gate-Source Voltage	±12	V
I <sub>D</sub> @T <sub>A</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 4.5V <sup>1</sup>	1.6	A
I <sub>D</sub> @T <sub>A</sub> =70°C	Continuous Drain Current, V <sub>GS</sub> @ 4.5V <sup>1</sup>	1.2	A
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	10	A
P <sub>D</sub> @T <sub>A</sub> =25°C	Total Power Dissipation <sup>3</sup>	0.37	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-ambient <sup>1</sup>	---	425	°C/W
R <sub>θJA</sub>	Thermal Resistance Junction-ambient <sup>1</sup> (t≤10s)	---	340	°C/W

### Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	20	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{\text{GS}}=4.5\text{V}$ , $I_D=1.5\text{A}$	---	---	80	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V}$ , $I_D=1.2\text{A}$	---	---	100	
		$V_{\text{GS}}=1.8\text{V}$ , $I_D=1\text{A}$	---	---	120	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D=250\mu\text{A}$	0.4	---	1.0	V
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=16\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\text{uA}$
		$V_{\text{DS}}=16\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=55^\circ\text{C}$	---	---	5	
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 12\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=5\text{V}$ , $I_D=1.5\text{A}$	---	5.5	---	S
$Q_g$	Total Gate Charge (4.5V)	$V_{\text{DS}}=15\text{V}$ , $V_{\text{GS}}=4.5\text{V}$ , $I_D=1.5\text{A}$	---	4.6	---	$\text{nC}$
$Q_{\text{gs}}$	Gate-Source Charge		---	0.7	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	1.5	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=10\text{V}$ , $V_{\text{GS}}=4.5\text{V}$ , $R_G=3.3\Omega$ $I_D=1.5\text{A}$	---	1.6	---	$\text{ns}$
$T_r$	Rise Time		---	42	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	14	---	
$T_f$	Fall Time		---	7	---	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=15\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	310	---	$\text{pF}$
$C_{\text{oss}}$	Output Capacitance		---	49	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	35	---	

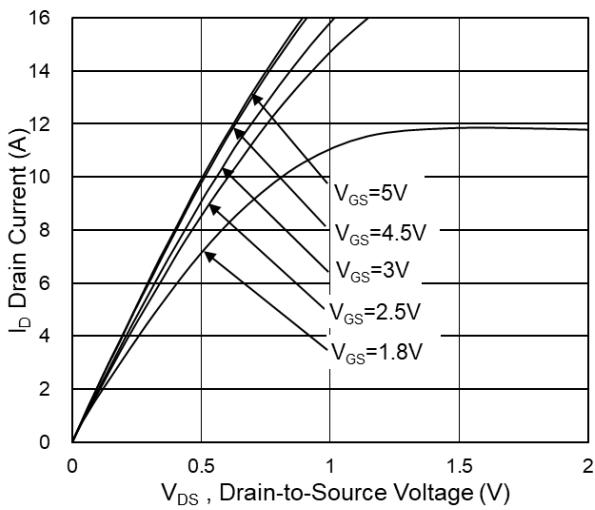
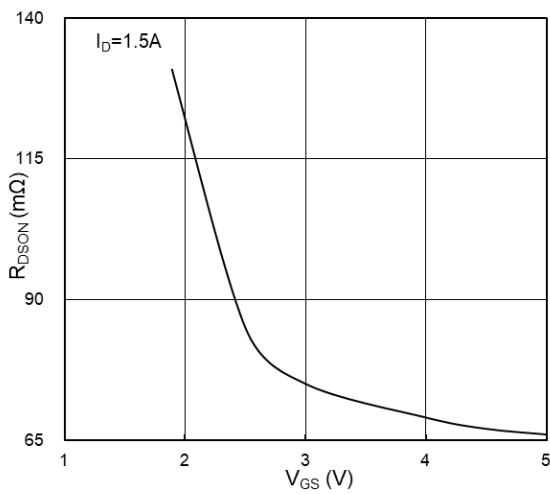
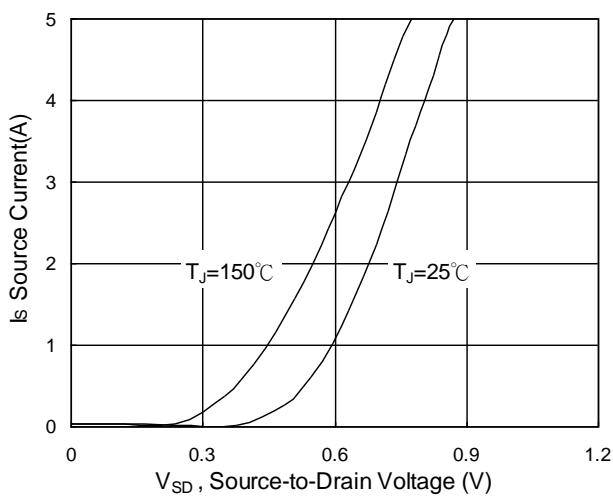
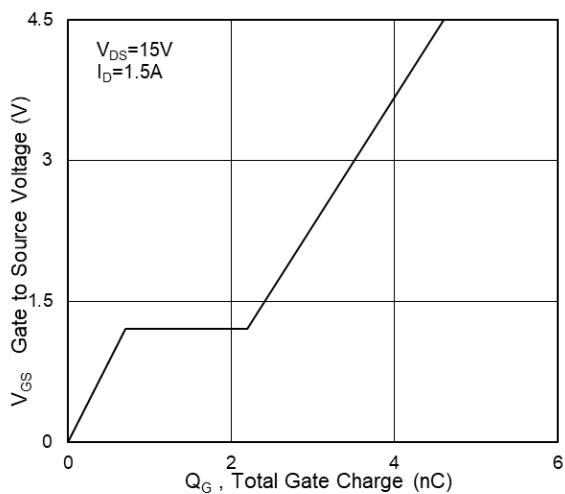
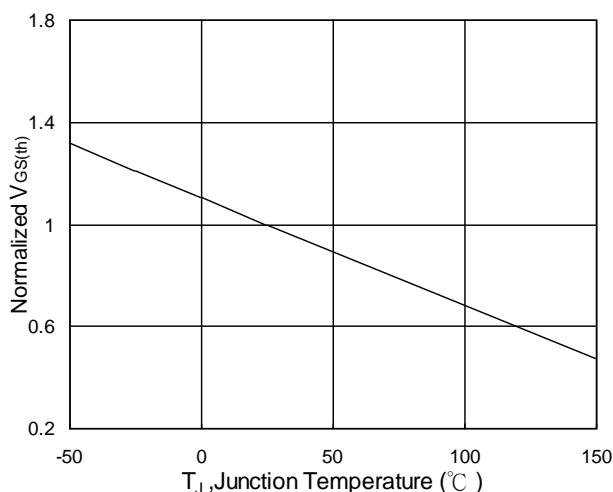
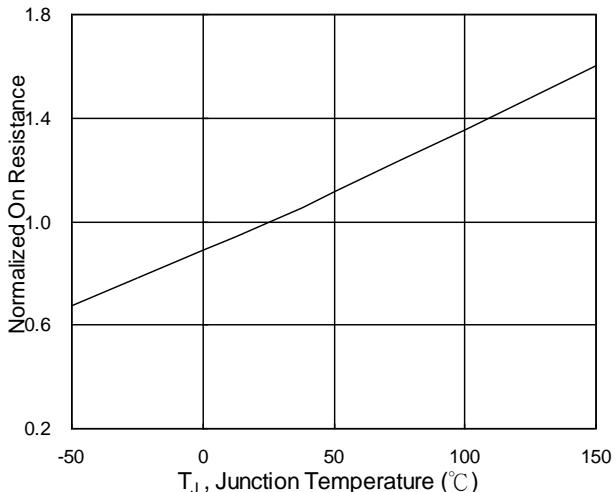
### Diode Characteristics

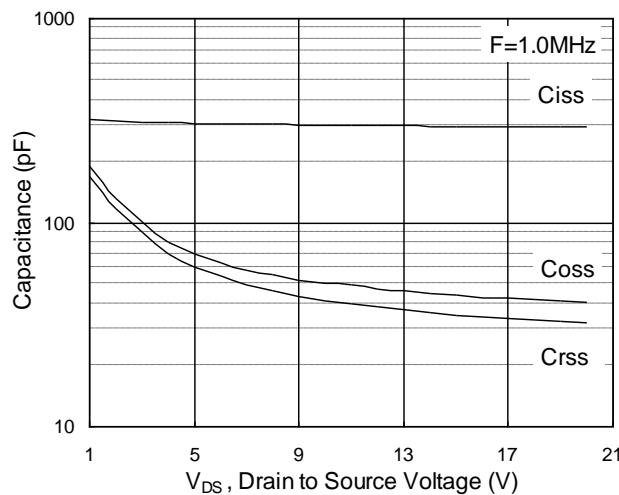
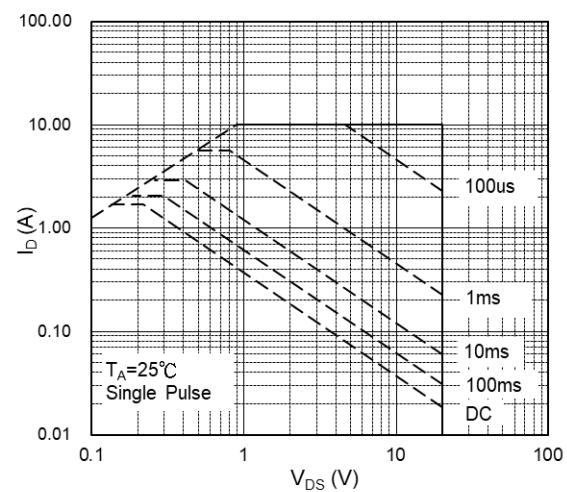
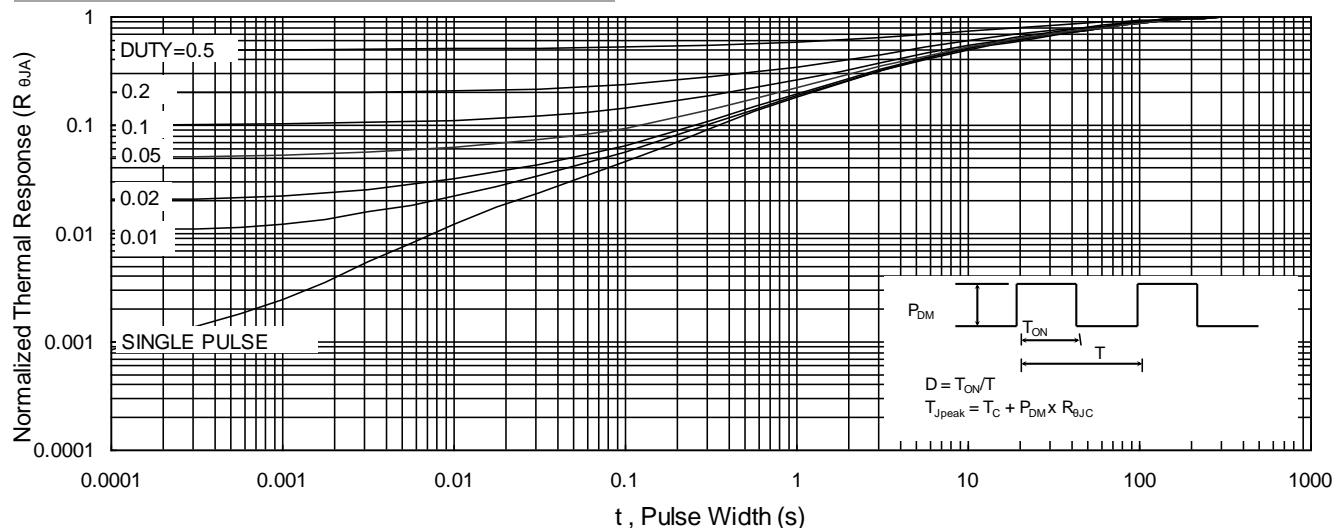
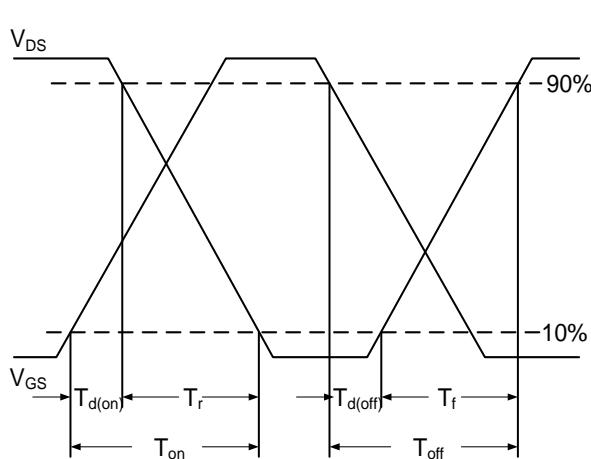
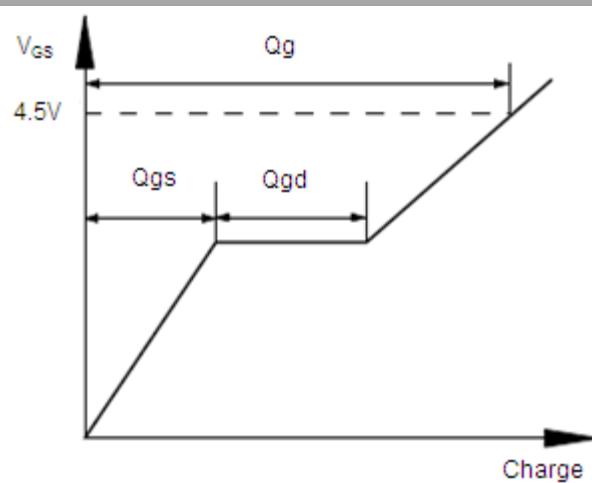
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current <sup>1,4</sup>	$V_G=V_D=0\text{V}$ , Force Current	---	---	3.6	A
$V_{\text{SD}}$	Diode Forward Voltage <sup>2</sup>	$V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1.2	V

Note :

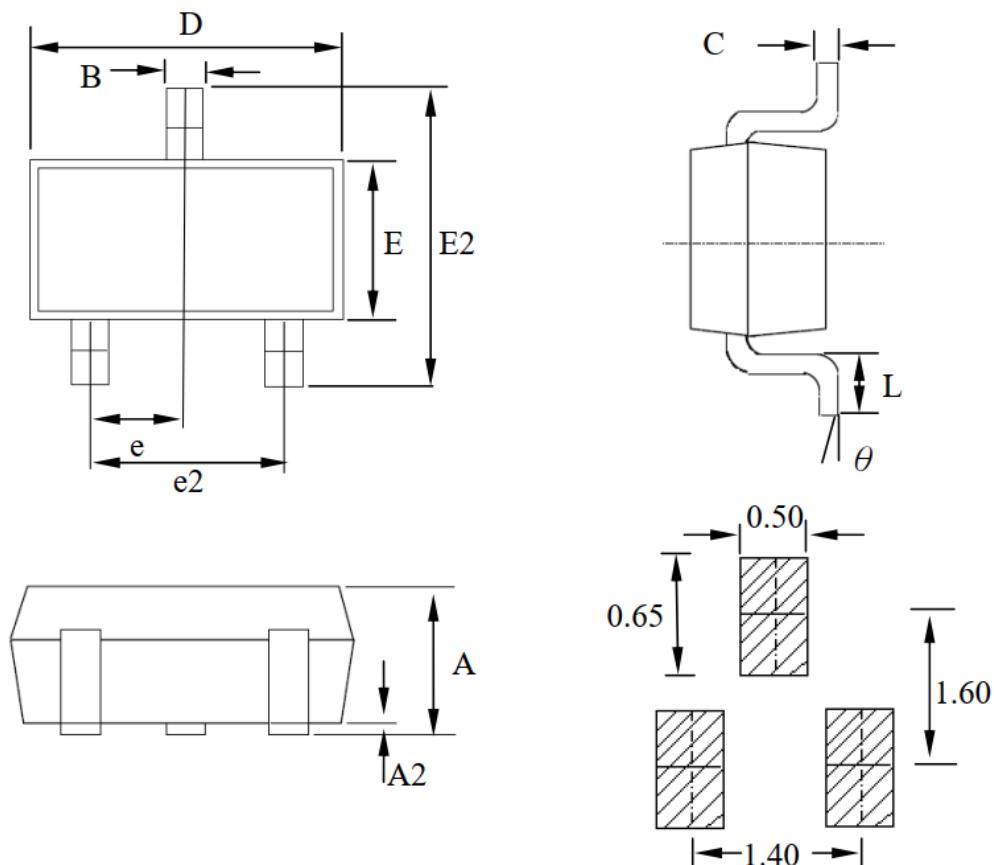
- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3.The power dissipation is limited by  $150^\circ\text{C}$  junction temperature
- 4.The data is theoretically the same as  $I_D$  and  $I_{\text{DM}}$  , in real applications , should be limited by total power dissipation.

### Typical Characteristics


**Fig.1 Typical Output Characteristics**

**Fig.2 On-Resistance vs G-S Voltage**

**Fig.3 Source Drain Forward Characteristics**

**Fig.4 Gate-Charge Characteristics**

**Fig.5 Normalized  $V_{GS(th)}$  vs  $T_J$** 

**Fig.6 Normalized  $R_{DS(on)}$  vs  $T_J$**


**Fig.7 Capacitance**

**Fig.8 Safe Operating Area**

**Fig.9 Normalized Maximum Transient Thermal Impedance**

**Fig.10 Switching Time Waveform**

**Fig.11 Gate Charge Waveform**

## SOT323 Package Outline Dimensions



LAND PATTERN RECOMMENDATION

SYMBOLS	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.90	--	1.10	0.035	--	0.043
A2	0.00	--	0.10	0.000	--	0.004
B	0.15	--	0.40	0.006	--	0.016
C	0.08	--	0.20	0.003	--	0.008
D	2.00	--	2.20	0.079	--	0.087
E	1.15	--	1.40	0.045	--	0.055
E2	2.15	--	2.45	0.085	--	0.096
e	--	0.65	--	--	0.026	--
e2	1.20	--	1.40	0.047	--	0.055
L	0.26	--	0.46	0.010	--	0.018
θ	0°	--	8°	0°	--	8°