

Features

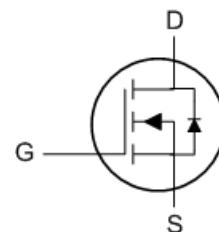
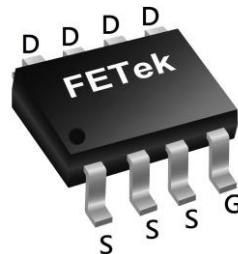
- Advanced Trench MOS Technology
- Fast Switching Speed
- 100% EAS Guaranteed
- Green Device Available

Product Summary

BVDSS	RDS(ON)	ID
100V	8mΩ	13.5A

Applications

- High Frequency Switching and Synchronous Rectification.
- DC/DC Converter.

SOP8 Pin Configuration**Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_A=25^\circ C$	Continuous Drain Current ¹	13.5	A
$I_D @ T_A=70^\circ C$	Continuous Drain Current ¹	10.5	A
I_{DM}	Pulsed Drain Current ²	55	A
EAS	Single Pulse Avalanche Energy ³	49	mJ
I_{AS}	Avalanche Current	14	A
$P_D @ T_C=25^\circ C$	Total Power Dissipation ⁴	3.1	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹ ($t \leq 10S$)	---	40	°C/W
	Thermal Resistance Junction-ambient ¹ (Steady State)	---	75	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-case ¹	---	24	°C/W



FETek Technology Corp.

FKS0048A

N-Ch 100V Fast Switching MOSFETs

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	100	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance ²	$V_{\text{GS}}=10\text{V}$, $I_D=13.5\text{A}$	---	6.6	8	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D=250\mu\text{A}$	2	---	4	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=80\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{\text{DS}}=80\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=5\text{V}$, $I_D=13.5\text{A}$	---	75	---	S
Q_g	Total Gate Charge	$V_{\text{DS}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_D=13.5\text{A}$	---	45	---	nC
Q_{gs}	Gate-Source Charge		---	9.5	---	
Q_{gd}	Gate-Drain Charge		---	4.8	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_G=3\Omega$, $I_D=13.5\text{A}$	---	10	---	ns
T_r	Rise Time		---	6.5	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	45	---	
T_f	Fall Time		---	7.5	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=50\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	3148	---	pF
C_{oss}	Output Capacitance		---	693	---	
C_{rss}	Reverse Transfer Capacitance		---	26	---	

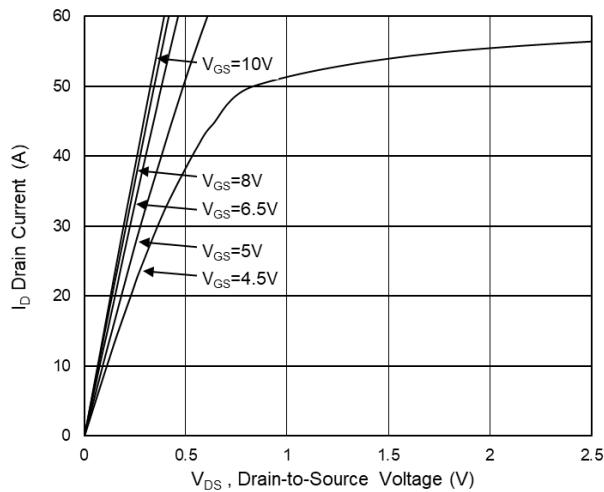
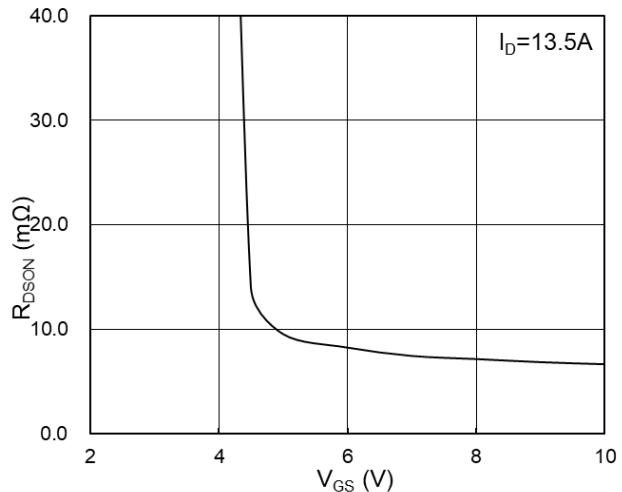
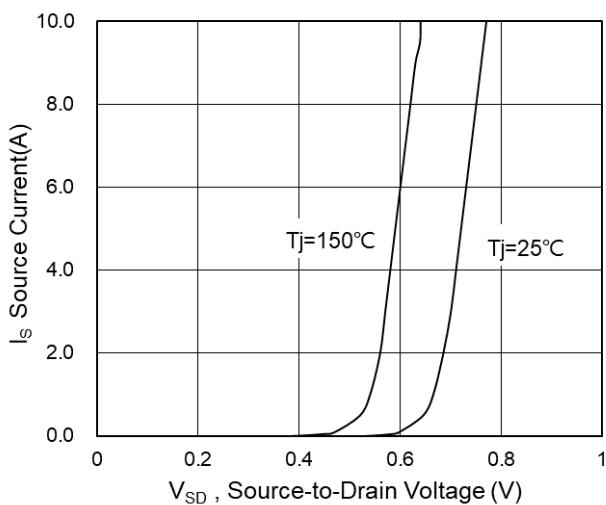
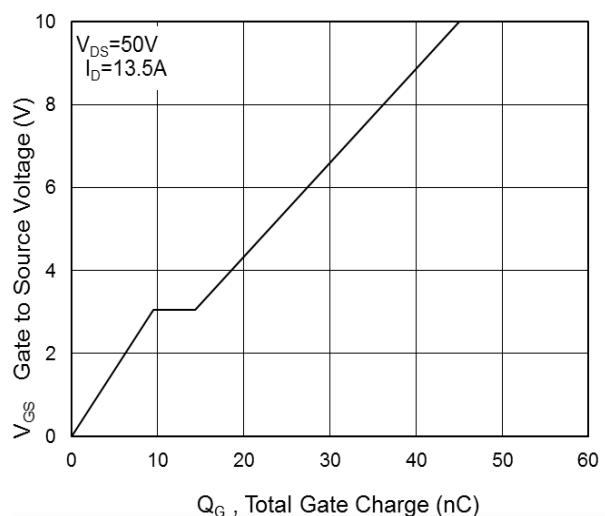
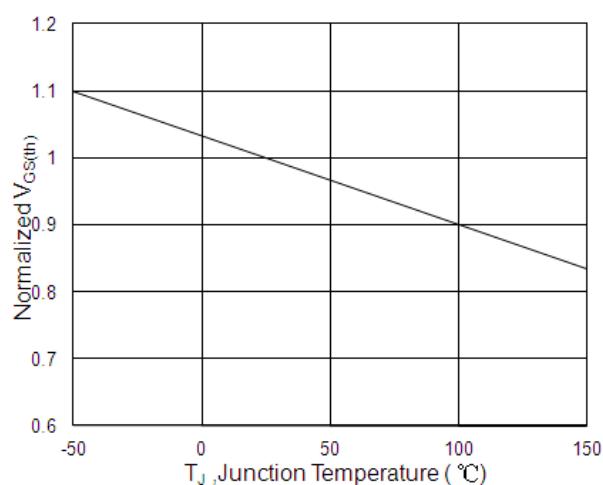
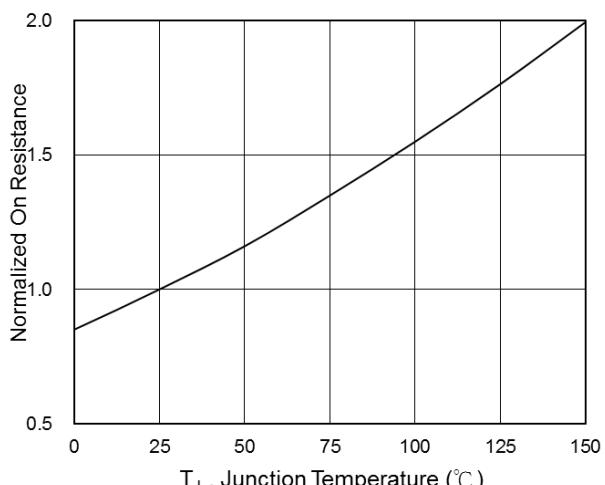
Diode Characteristics

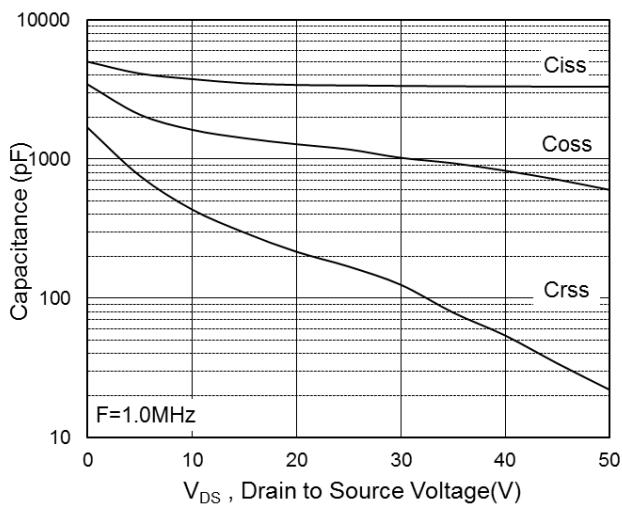
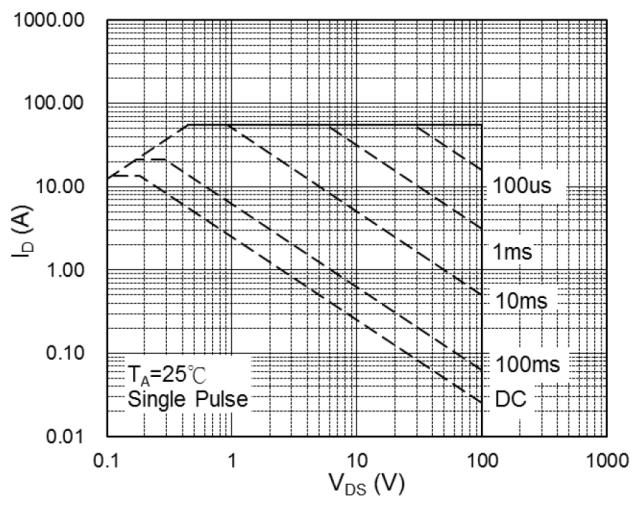
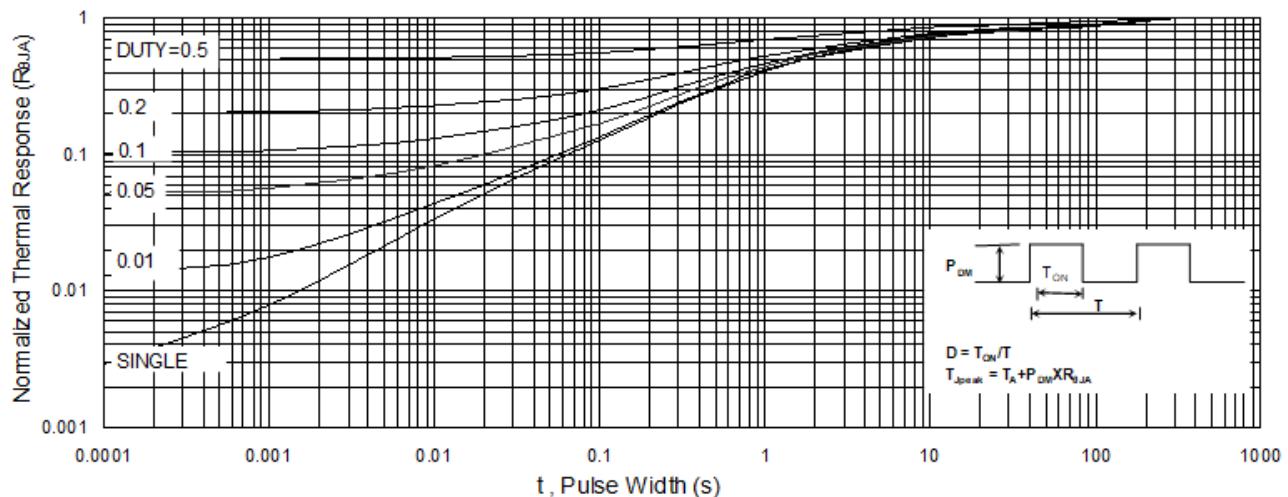
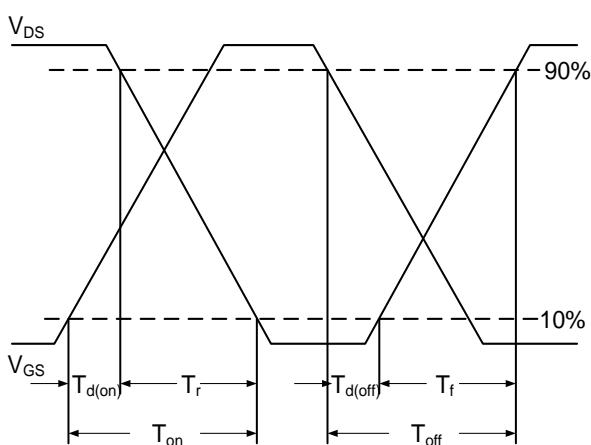
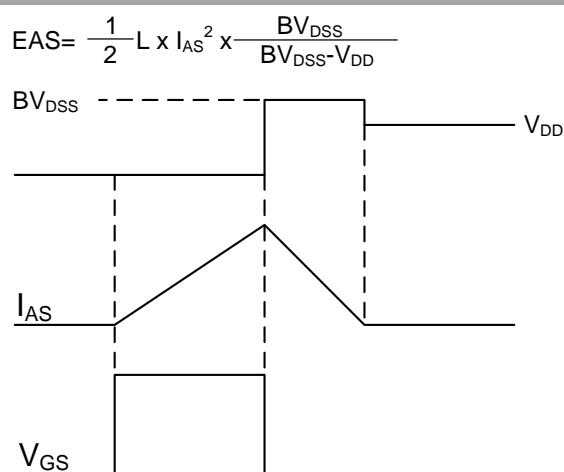
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current ^{1,5,6}	$V_G=V_D=0\text{V}$, Force Current	---	---	13.5	A
V_{SD}	Diode Forward Voltage ²	$V_{\text{GS}}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.1	V
t_{rr}	Reverse Recovery Time	$I_F=13.5\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$,	---	33	---	nS
		$T_J=25^\circ\text{C}$	---	150	---	nC
Q_{rr}	Reverse Recovery Charge					

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=0.5\text{mH}$, $I_{\text{AS}}=14\text{A}$
- 4.The power dissipation is limited by 175°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics


Fig.1 Typical Output Characteristics

Fig.2 On-Resistance vs G-S Voltage

Fig.3 Source Drain Forward Characteristics

Fig.4 Gate-Charge Characteristics

Fig.5 Normalized $V_{GS(th)}$ vs T_J

Fig.6 Normalized $R_{DS(on)}$ vs T_J


Fig.7 Capacitance

Fig.8 Safe Operating Area

Fig.9 Normalized Maximum Transient Thermal Impedance

Fig.10 Switching Time Waveform

Fig.11 Unclamped Inductive Waveform