



- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent Cdv/dt effect decline
- ★ Advanced high cell density Trench technology

Product Summary



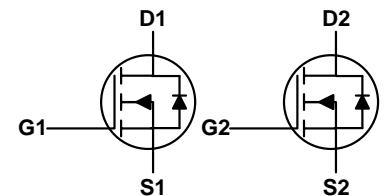
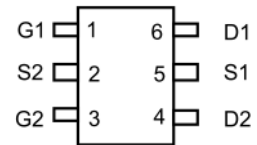
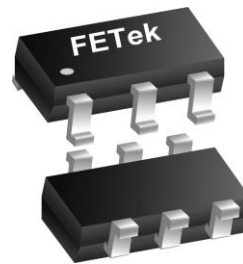
BVDSS	R _{DS(on)}	I _D
30V	60mΩ	3.8A

Description

The FKQ3706 is the high performance complementary N-ch MOSFETs with high cell density, which provides excellent R_{DS(on)} and efficiency for most of the small power switching and load switch applications.

The FKQ3706 meet the RoHS and Green Product requirement with full function reliability approved.

TSOP6 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	30	V
V _{GS}	Gate-Source Voltage	±12	V
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ 4.5V ¹	3.8	A
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ 4.5V ¹	3	A
I _{DM}	Pulsed Drain Current ²	16	A
P _D @T _C =25°C	Total Power Dissipation ³	1.1	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient ¹	---	110	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	70	°C/W

**N-Channel Electrical Characteristics (T_J=25 °C, unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	30	---	---	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =3.4A	---	52	60	mΩ
		V _{GS} =4.5V, I _D =3A	---	57	65	
		V _{GS} =2.5V, I _D =2A	---	70	85	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	0.4	---	1.2	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =24V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =24V, V _{GS} =0V, T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±12V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =3.4A	---	6	---	S
Q _g	Total Gate Charge (4.5V)	V _{DS} =15V, V _{GS} =4.5V, I _D =3A	---	8.4	---	nC
Q _{gs}	Gate-Source Charge		---	1.6	---	
Q _{gd}	Gate-Drain Charge		---	1.8	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =10V, V _{GS} =4.5V, R _G =3.3Ω I _D =3A	---	3.2	---	ns
T _r	Rise Time		---	41.8	---	
T _{d(off)}	Turn-Off Delay Time		---	21.2	---	
T _f	Fall Time		---	6.4	---	
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz	---	662	---	pF
C _{oss}	Output Capacitance		---	52	---	
C _{rss}	Reverse Transfer Capacitance		---	45	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,4}	V _G =V _D =0V, Force Current	---	---	3.8	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =1A, T _J =25°C	---	---	1.2	V

Note :

1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

2.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%

3.The power dissipation is limited by 150°C junction temperature

4.The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

N-Channel Typical Characteristics

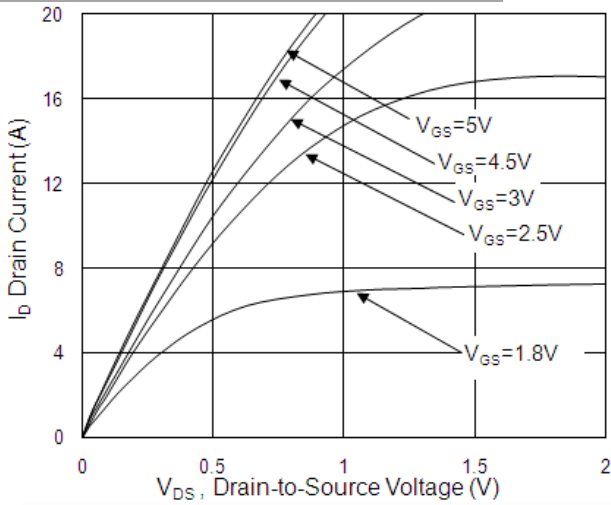


Fig.1 Typical Output Characteristics

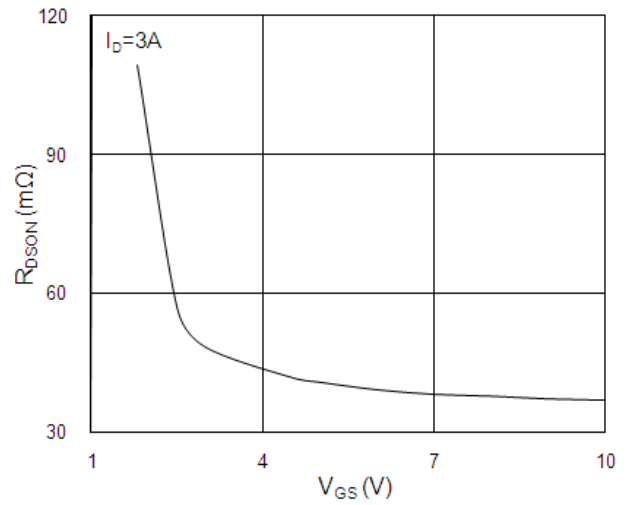


Fig.2 On-Resistance vs. Gate-Source

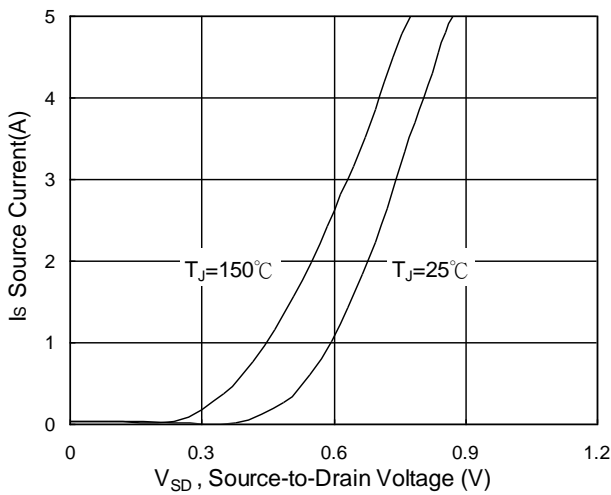


Fig.3 Forward Characteristics of Reverse

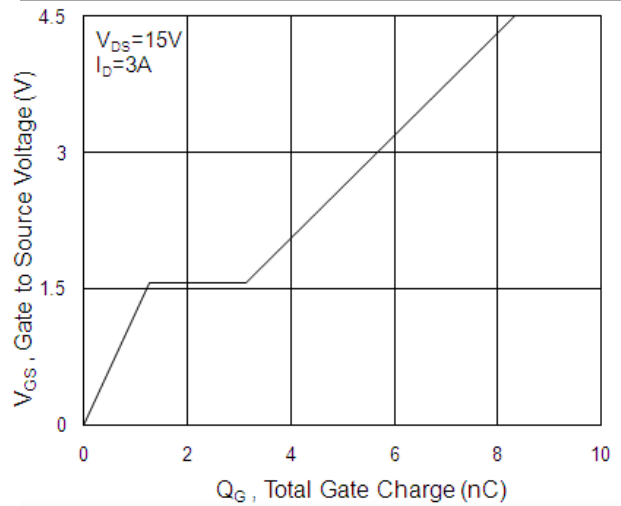


Fig.4 Gate-Charge Characteristics

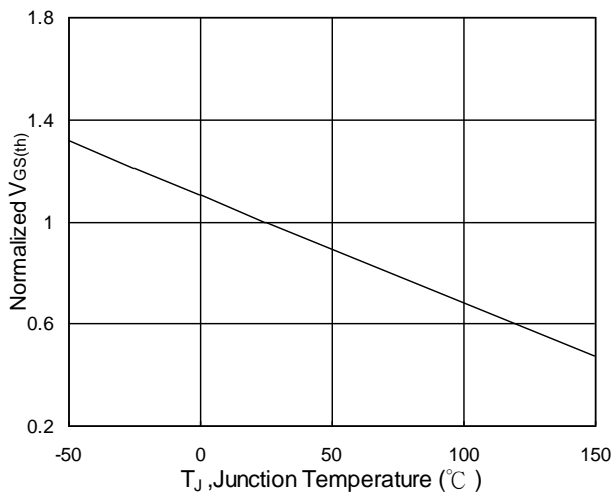


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

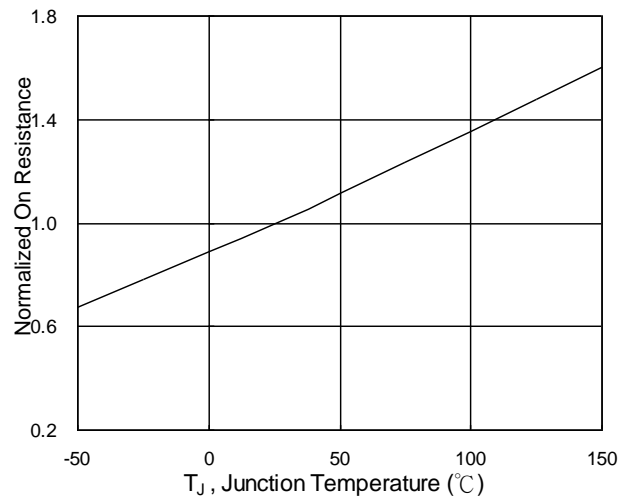


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

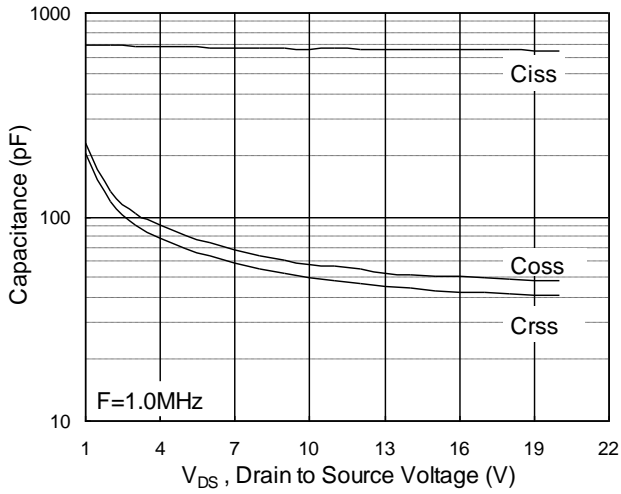


Fig.7 Capacitance

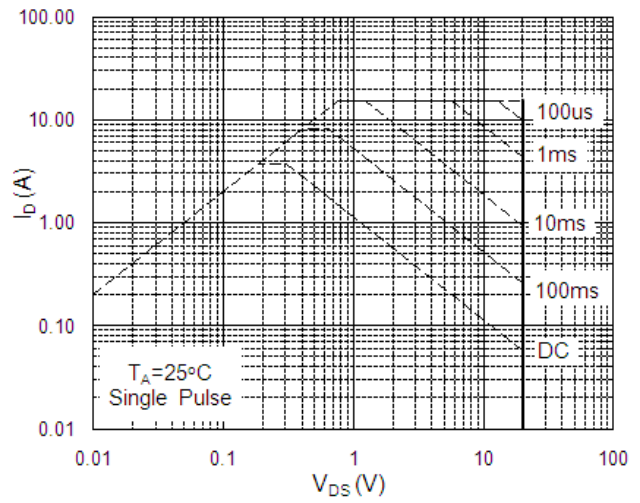


Fig.8 Safe Operating Area

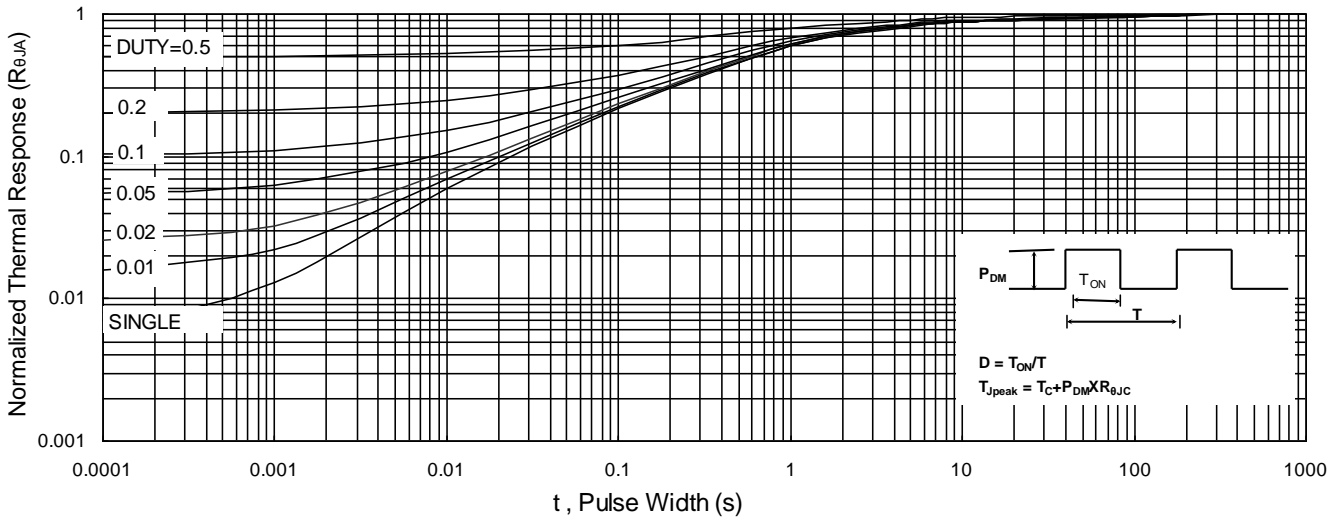


Fig.9 Normalized Maximum Transient Thermal Impedance

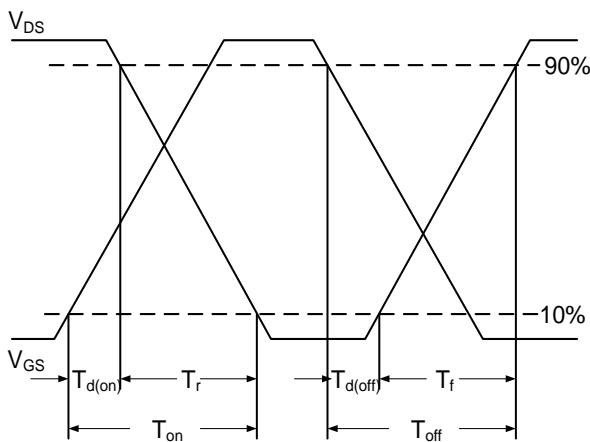


Fig.10 Switching Time Waveform

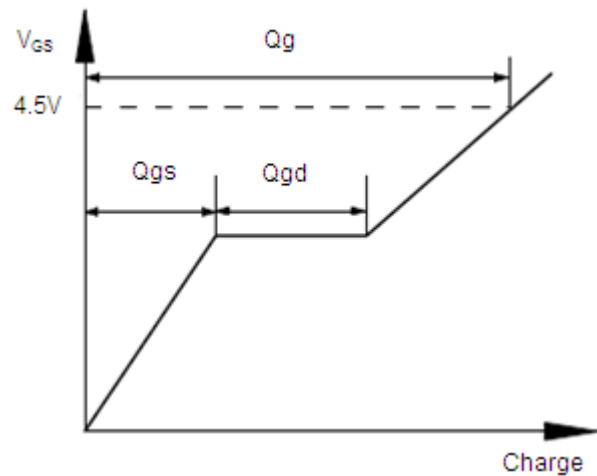


Fig.11 Gate Charge Waveform